



INTERNATIONAL APPLICATION PUBLISHED UNDER THE PATENT COOPERATION TREATY (PCT)

(51) International Patent Classification ⁶ : G09G 3/32	A1	(11) International Publication Number: WO 99/49446 (43) International Publication Date: 30 September 1999 (30.09.99)
<p>(21) International Application Number: PCT/IB99/00891</p> <p>(22) International Filing Date: 19 March 1999 (19.03.99)</p> <p>(30) Priority Data: 60/078,685 20 March 1998 (20.03.98) US</p> <p>(71) Applicant (for all designated States except US): VERSITECH LTD. [CN/CN]; G18 Eliot Hall, The University of Hong Kong, Pokfulam Road, Hong Kong (CN).</p> <p>(72) Inventors; and (75) Inventors/Applicants (for US only): PANG, Grantham, Kwok-Hung [CA/CN]; B5, 5/F Block 1, Skyline Mansion, 51 Conduit Road, Mid-Levels, Hong Kong (CN). YANG, Edward, S. [US/US]; 1100 Cotton Street, Menlo Park, CA 94025 (US). KWAN, To-On [CN/CN]; Room 1407, Kwai Chi House, Kwai Fong Estate, Kwai Chung, Hong Kong (CN). CHAN, Chi-Ho [GB/CN]; 2/F, 1 Hing Yin Street, Tokwawan, Kowloon, Hong Kong (CN). HO, Ka-Lim [GB/CN]; Flat A, 11/F., Block 1, Fok On Garden, Ma On Shan, Hong Kong (CN).</p> <p>(74) Agents: COCHRAN, David, B. et al.; Jones, Day, Reavis & Pogue, North Point, 901 Lakeside Avenue, Cleveland, OH 44114 (US).</p>		<p>(81) Designated States: CN, JP, US, European patent (AT, BE, CH, CY, DE, DK, ES, FI, FR, GB, GR, IE, IT, LU, MC, NL, PT, SE).</p> <p>Published With international search report. Before the expiration of the time limit for amending the claims and to be republished in the event of the receipt of amendments.</p>
<p>(54) Title: TRICOLOR LED DISPLAY SYSTEM HAVING AUDIO OUTPUT</p> <div data-bbox="406 1134 1218 1722"> <pre> graph LR 12[COMPUTER 12] --- 14[] --- 18[INTERFACE CIRCUIT 18] 16[AUDIO SIGNAL TRANSMITTER 16] --- 18 18 --- 20[TRICOLOR LED DOT MATRIX DISPLAY 20] 20 -. 24[RECEIVER 24] .- </pre> </div>		

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Tricolor LED Display System Having Audio Output

RELATED APPLICATIONS

This application claims priority from provisional application serial number US
5 60/078,685 filed in the United States within one year from the filing date of this
application.

FIELD OF THE INVENTION

The present invention relates to the field of LED display systems and data
communications. More specifically, the invention provides a tricolor light emitting
10 diode ("LED") display system in which the LEDs are used as concurrent display and
data communication elements for displaying visible information and for transmitting
modulated audio information to an associated receiver.

SUMMARY OF THE INVENTION

15 A tricolor LED display system is provided having a plurality of multi-color
LEDs that operate as concurrent display and data communication elements. These
dual-use tricolor LEDs are coupled to a special-purpose computer system and audio
signal transmitter for simultaneously controlling the visible display signal from the
LEDs, and for modulating audio information onto the visible light signal emitted by
20 the LEDs. A receiver receives the modulated visible light signal from the LEDs and
demodulates the received signal to recover the audio information, which is then
presented to a user of the system through an audio interface.

The LED display system preferably includes a transmitter sub-system and a
receiver sub-system. The transmitter subsystem may include a special-purpose
25 computer, such as a PC, workstation or embedded computer, and an audio signal
transmitter, which are both coupled to the preferred tricolor dot matrix LED display
via a special interface circuit. The audio signal transmitter preferably includes a
voltage-controlled oscillator for modulating the audio signal information onto the
LEDs of the display. The preferred tricolor LED display is capable of generating
30 three distinct colors – red, green and amber. The receiver subsystem may include a
lens for focusing the transmitted light from the LEDs onto a photo-detector (or other
optical detection means), a demodulator circuit for recovering the audio information

from the modulated visible light signal emitted by the tricolor display, and an audio system for presenting the demodulated audio information to a user of the system.

According to one aspect of the invention, a tricolor LED display system is provided that includes a transmitter sub-system for simultaneously transmitting
5 visible display signals and modulated visible light signals through a tricolor LED display, wherein the modulated visible light signals carry modulated audio information; and a receiver sub-system for receiving the modulated visible light signals and for demodulating the visible light signals to recover the transmitted audio information.

10 According to another aspect of the invention, a tricolor LED display system is provided that includes a tricolor LED dot matrix display having a plurality of tricolor LEDs; means for applying a display signal to the tricolor LEDs to generate a visible signal pattern; and means for applying an audio signal to the tricolor LEDs to emit a modulated visible light signal that carries the audio signal.

15 Still another aspect of the invention provides an interface circuit for controlling a tricolor LED display, comprising: a row counter decode system coupled to a modulated audio signal for controlling the row selection of the LEDs in the tricolor LED display; and a segment counter decode system coupled to display information for controlling the segment selection of the LEDs in the tricolor LED
20 display. The interface may also include: a parallel port interface for receiving the display information, the display information including display data and addressing information for the LED display; an address comparison unit for comparing the addressing information for the LED display with system addressing information generated by the row counter decode system and the segment counter decode system;
25 and a pattern storage system for storing the display data when the address comparison unit indicates that the addressing information for the LED display is the same as the addressing information generated by the row counter decode system and the segment counter decode system.

It should be noted that these are just some of the many aspects of the present
30 invention. Other aspects not specifically listed will become apparent upon reading the detailed description set forth below.

The present invention provides many advantages over presently known data communication systems and LED display systems. Not all of these advantages are simultaneously required to practice the invention as claimed, and the following list is merely illustrative of the types of benefits that may be provided, alone or in combination. A primary advantage of the present invention is in the use of tricolor LEDs as the concurrent display and data communication elements. This provides a data communication system that is more reliable. Because tricolor LEDs are utilized in the display of the invention, the background of the display can be energized using a color that is different from the main characters (or messages) being displayed. LEDs in a given row (regardless of color state) can be modulated with the same audio information. By constructing the system in this manner, there will always be an LED lit up in each row, thus ensuring more reliable data transmission over single-color displays, in which certain LEDs in a row have to be shut off in order to provide the correct visible display. In addition to this reliability advantage, the pattern or graphics displayed by the tricolor display will also be more attractive and flexible as more colors can be used for the display.

Other advantages of this invention include, for example: (1) a brighter display than a single-color LED display; (2) reduced noise on the modulated signal due to the design of the interface circuit and its synchronization source; (3) permits text scrolling with no effect on the transmitted modulated signal; and (4) relaxed design constraints on the receiver system.

These are just a few of the many advantages of the present invention, as described in more detail below in terms of the preferred embodiments. As will be appreciated, the invention is capable of other and different embodiments than those specifically set forth below, and its details are capable of modifications in various respects, all without departing from the spirit of the invention. Accordingly, the drawings and description of the preferred embodiments are to be regarded as illustrative in nature and not restrictive.

BRIEF DESCRIPTION OF THE DRAWINGS

FIG. 1 is a block diagram of a preferred tricolor LED display system according to the present invention, including a transmitter sub-system and a receiver sub-system;

5 FIG. 2 is a block diagram of an audio signal transmitter, which is part of the preferred transmitter sub-system shown in FIG. 1;

FIG. 3 is a block diagram of a preferred receiver sub-system;

FIG. 4 is a block diagram of a preferred interface circuit, which is part of the preferred transmitter sub-system shown in FIG. 1; and

10 FIG. 5 is a schematic of a tricolor dot matrix display using light emitting diodes.

DETAILED DESCRIPTION OF THE DRAWINGS

Turning now to the drawing figures, Figure 1 sets forth a block diagram of a preferred tricolor LED display system 10 according to the present invention, including a transmitter sub-system 12-22 and a receiver sub-system 24. The transmitter sub-system includes a computer 12, which may be operating an executive computer program 14, an audio signal transmitter 16, an interface circuit 18, and a tricolor LED display 20 comprising a plurality of tricolor LEDs 22. The receiver sub-system 24 is described in more detail below in connection with Figure 3.

The computer 12 could be a PC, workstation, laptop, embedded computer, or any other type of processing system capable of executing a program. The executive computer program 14 runs on the computer 12, and controls the visual display of characters, decorative patterns or messages on the display panel 20. The audio signal transmitter 16 (described in more detail below in connection with Figure 2) generates the modulated audio signal that is applied to the LED display 20 in order to cause the LEDs 22 to emit the modulated visible light signal that carries the desired audio information. The audio signal transmitter 16 and the computer 12 are coupled to the tricolor LED dot matrix display 20 by the interface circuit 18. The coupling between the computer 12 and the interface circuit 18 is preferably a parallel data port for transporting parallel data from the computer 12 to the interface circuit 18, which uses this data to generate the appropriate display message on the tricolor LED display

matrix 20. The preferred interface circuit is described in more detail below in connection with Figure 4. The purpose of this circuit is to simultaneously control the display and data communication functions of the LED display 20.

5 The receiver sub-system 24 includes elements for receiving the modulated visible light signal from the tricolor display 20, and for demodulating the received signal in order to recover the desired audio information. The preferred elements of the receiver sub-system are described in Figure 3.

Figure 2 is a block diagram of an audio signal transmitter 16 that forms part of the preferred tricolor LED display system shown in Figure 1. The audio signal
10 transmitter includes a source of audio information, such as, for example, a cassette tape 30 or CD player 32, a voltage controlled oscillator ("VCO") circuit 34 and a buffer circuit 36. The output of the audio signal transmitter is a modulation signal 38, which, when applied to the LEDs 22 of the tricolor LED display 20, causes the LEDs to emit the modulated visible light signal that includes the audio information.

15 The cassette tape 30 or CD player 32 generates audio signals that are to be transmitted by the LED display 22. This audio signal is then fed to the VCO 34, which operates as a frequency modulator ("FM") in order to modulate the audio signal so that a modulated carrier frequency is proportional to the amplitude of the audio signal. The VCO outputs the modulation signal 38, which is applied to the
20 LEDs 22 in order to turn the LEDs on/off at a particular frequency corresponding to the modulation signal. This frequency is high enough such that the perceivable light from the LEDs 22 appears to be constantly illuminated to the human eye. Thus, to an observer there is no indication that the LEDs of the tricolor display are operating in a concurrent manner.

25 Figure 3 is a block diagram of a preferred receiver sub-system 24. The receiver sub-system 24 includes a lens 42, photo-detector 44, FM demodulator 46 and a speaker 50 and/or headphones 48. The lens 42 collects the visible light emitted by the LED display 20, including the modulated visible light signal. These light rays are then focused onto a photo-detector 44, or other type of optical detection means. The
30 photo-detector 44 converts the modulated visible light signal into an electrical signal. The FM demodulator 46 then demodulates the modulated signal into the original audio signal generated by cassette 30 or CD player 32. A speaker 50, headphones 48

or ear jack can be used for the delivery of the audio messages or music sent from the transmitter 16. The receiver sub-system could be battery powered, such that it is portable, or could be powered by any other type of electrical system.

Figure 4 is a block diagram of a preferred interface circuit 18 that forms part of the preferred transmitter sub-system shown in Figure 1. This circuit 18 may include a row counter decode system 60; a parallel port interface circuit 64; a status indicator 66; a segment counter decode system 68; a pattern storage system 72; an address comparison circuit 78; and an oscillator 80. The circuit may also include one or more buffer/register circuits 62, 70, 74, 76. Three of these buffer/registers 62, 74 and 76 generate the row data, segment data and segment selection signals, respectively, that control the tricolor LED display 20. The interface circuit 18 is coupled to the computer 12 by parallel port interface 64 and to the audio signal transmitter by modulation signal 38.

The modulation signal 38 from the audio signal transmitter 16 is coupled to the row counter decode system 60, which is in turn coupled to the row data input on the tricolor LED display 20 by optional buffer/register 62. The row counter decode system 60 is used to switch among the various rows of the LED display 20, and includes a counter driven by the modulation signal 38, and a decoder for appropriate row selection.

The segment counter decode system 68 is driven by oscillator 80. It is used to switch among the segments of the LED display 20. The segment counter decode system 68 is coupled to the segment selection input on the tricolor LED display 20 by optional buffer/register 76. This circuit 68 includes a counter, which is driven by a fast oscillator 80, and a decoder for segment selection.

The pattern storage system 72 is used to store the LED display pattern of every row and segment in the matrix 20. It is preferably an SRAM, although other types of solid state memory could also be used for this part. The data pattern stored in the pattern storage system 72 is provided by the computer system 12 via parallel port interface 64.

The address comparison unit 78 takes as inputs the current row address (from the row counter decode system 60) and the segment address (from the segment counter decode system 68) and compares these addresses to the address signal from

the parallel port interface 64. It compares the system-generated address (based on the two counter systems 60, 68) against the address sent out by the computer 12. This is done to ensure that the SRAM data stored in the pattern storage system 72 and the display 20 will not be disrupted until the LEDs are in the inactive state (off state).
5 Until these address values match, new data is not written into the pattern storage system 72. The address comparison unit 78 includes a bit comparator (for comparing the bits of the address values) and a data register 70. The register holds the message data from the parallel port interface 64 until the system is ready to write data to the SRAM (*i.e.*, when the address values match). When this occurs, the address
10 comparison unit asserts the "/WE" line to the SRAM 72, which causes the register data 70 to be written into the pattern storage system 72. The pattern storage system outputs the segment data to the appropriate LEDs in the tricolor display 20 through an optional buffer/register circuit 74.

The status indicator 66 includes a D-type flip-flop with asynchronous preset
15 and clear inputs. The non-inverted flip-flop output pin (strobe pin) is used to indicate a successful write operation. When this occurs (based on a signal from the address comparison unit 78), the strobe pin will be set high, which is then transmitted to the computer 12 via the parallel port interface 64 so that the computer 12 can send out the next data.

20 Operationally, the interface circuit 18 updates the display pattern of the tricolor LED display 20 as follows. First, the data from the computer 12 is latched into a register 70. However, the data will not be sent to the SRAM 72 input pins or the segment register 74 input pins so as not to disrupt the normal LED display process. Second, the address from the computer 12 is presented to the address
25 comparator 78, which compares this address against the system-generated address from the row counter decode system 60 and the segment counter decode system 68. When the addresses match, the output of the comparator is pulled low (/WE), which causes the data stored in the register 70 to be written to the pattern storage SRAM 72. Once the write is complete, a signal ("P=Q") from the address comparison unit 78 is
30 sent to the status indicator circuit 66, which sets the strobe output high, thus informing the computer 12 that the write succeeded, and that additional data may be presented to the interface circuit 18.

Having described the preferred circuit structure of the interface circuit 18, it is instructive to consider two additional operational characteristics of this design that provide advantages over presently known LED display systems. The first advantage is the use of multiple clock signals for the LED display, including a high-speed oscillator clock 80 and a modulation clock signal 38. Whenever the modulation signal changes, the active row of the LED display 20 will also be changed. Thus, the row represents a bit of information. The preferred frequency of the modulation signal is 100 kHz. Audio signal information is modulated onto this signal by the VCO 34 of the transmitter sub-system, thus resulting in a frequency range of 100 ± 44.1 kHz.

The other timing signal used in the preferred interface circuit 18 is generated from the high-speed oscillator 18. In the preferred implementation, a 12 MHz oscillator frequency is used, although other frequencies could also be selected. This signal is used to switch between different segments of the tricolor LED display 20 (*i.e.*, different colors in different characters). The segment clock should operate at a higher frequency than the modulation signal clock 38 so that before the modulation signal 38 is changed (*i.e.*, before the active row is switched), all the segments have been scanned at least once and updated. Otherwise, some segments will not be lit as expected.

While the common signals 92 ("row") are changed at the frequency of the modulation signal (*e.g.*, 100 kHz), the segment signals 90, 94 ("column") are changed at the higher frequency of about 12 MHz. However, this 12 MHz may be divided by the segment counter 68 so that it is operating at 6 MHz. In addition, only half of the 100 kHz period of the modulation signal is used for segment selection. Therefore, during the period that a row is active, the number of times that the segment address is changed is approximated by:

$$\frac{6M}{100k} * \frac{1}{2} = 30 \quad [1]$$

The second operational characteristic of the preferred interface circuit 18 is that updating of the pattern data is carried out during the period that the LEDs are in the off state. This eliminates any noise on the emitted LED signals by synchronizing the circuit with the modulation signal 38, including the time when the pattern is updated. The interface circuit 18 updates the display only when the row input to the

LED 20 is inactive. In this manner, no noise will be injected into the modulation signal because no data will be transmitted during the pattern update process.

Figure 5 is a schematic of a tricolor dot matrix 20 display using light emitting diodes 22 that are capable of emitting three different colors. These colors are preferably red, green and amber. The individual LEDs 22 are coupled to common signals 92, which tie the LEDs in a given row together, and two segment signals, one for red 90 and another for green 94. To energize an LED, the common signal 92 for that row is held low. Then, if a red signal is to be emitted, just the red segment 90 for the particular column is held high. If a green signal is to be emitted, just the green segment 94 for the particular column is held high. And if an amber signal is to be emitted, both the red 90 and green 94 segments are held high.

The software program 14 used for the display control and pattern update of this tricolor LED dot matrix display 20 operates as follows. For the write cycle, data is first put onto the parallel port bus coupling the computer 12 to the interface circuit 18, which latches this data. The corresponding address (*i.e.*, row and segment data) is then put onto the same bus by the program 14. This address is compared against the system-generated address by the interface circuit 18. If the addresses match, then the message data will be written to the pattern storage system 72 of the interface circuit 18 at that address. A signal will be sent back to the computer 12, indicating a successful write. Since all the LEDs are then illuminated, it is now possible to introduce display effects, such as scrolling into the system.

The benefits of this invention can be applied to a large variety of applications. In an indoor environment, a user with an appropriate receiver system 24 could listen to the audio messages broadcast through the tricolor LEDs 22 of the dot matrix display 20. A quiet atmosphere can be maintained. A major advantage over a conventional broadcasting system is that an individual with a receiver has the freedom of choice in receiving specific messages without hearing any unwanted announcement, music or commercials.

In an outdoor environment, for example in front of a business or commercial institution on the street, the light emitted from the tricolor dot matrix LED display 20 could be used to optically transmit audio information to a user with a receiver and headphones located some distance away from the institution. With this invention, all

commercial billboards could be used to transmit additional information such as daily specials, discounts, latest attractions or any merchandise, reservation telephone numbers, etc.

5 The preferred embodiments and several applications of the inventive system described above are presented only by way of example and are not meant to limit the scope of the present invention, which is defined by the claims. Other elements and steps could be used in place of those shown.

What is claimed:

1. A tricolor LED display system, comprising:
 - a transmitter sub-system for simultaneously transmitting visible display
 - 5 signals and modulated visible light signals through a tricolor LED display, wherein the modulated visible light signals carry modulated audio information; and
 - a receiver sub-system for receiving the modulated visible light signals and for demodulating the visible light signals to recover the transmitted audio information.
- 10 2. The system of claim 1, wherein the transmitter sub-system includes:
 - a computer system for generating display information;
 - an audio signal transmitter for generating a modulated audio signal; and
 - an interface circuit coupled to the computer system and the audio signal transmitter for applying the display information and the modulated audio signal to the
 - 15 tricolor LED display.
3. The system of claim 1, wherein the tricolor LED display is capable of displaying red, green and amber colors.
- 20 4. The system of claim 2, wherein the computer system is coupled to the interface circuit through a parallel port interface.
5. The system of claim 2, further including an executive computer program operating on the computer system for controlling the generation of display information.
- 25 6. The system of claim 2, wherein the audio signal transmitter includes:
 - a source of audio information for generating an audio signal; and
 - a modulator coupled to the source of audio information for generating the modulated audio signal.
- 30 7. The system of claim 6, wherein the modulator is a voltage controlled oscillator.

8. The system of claim 1, wherein the receiver sub-system includes:

an optical sensor for receiving the modulated visible light signal from the tricolor LED display;

5 a demodulator coupled to the optical sensor for demodulating the modulated visible light signal to recover the transmitted audio signal; and
a speaker coupled to the audio signal.

9. The system of claim 8, wherein the receiver sub-system further includes a lens for focusing light emitted by the tricolor LED display onto the optical sensor.

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10. The system of claim 8, wherein the optical sensor is a photo-detector.

11. The system of claim 2, wherein the interface circuit includes:

15

a row counter decode system coupled to the modulated audio signal for controlling the row selection of the LEDs in the tricolor LED display; and

a segment counter decode system coupled to the display information for controlling the segment selection of the LEDs in the tricolor LED display.

20

12. The system of claim 11, wherein the interface circuit further includes:

a parallel port interface for receiving the display information, the display information including display data and addressing information for the LED display;

an address comparison unit for comparing the addressing information for the LED display with system addressing information generated by the row counter

25

decode system and the segment counter decode system; and

a pattern storage system for storing the display data when the address comparison unit indicates that the addressing information for the LED display is the same as the addressing information generated by the row counter decode system and the segment counter decode system.

30

13. The system of claim 12, wherein the interface circuit further includes:

a status indicator coupled to the parallel port interface for generating a write complete signal to the computer system when the display data is written to the pattern storage system.

- 5 14. The system of claim 7, wherein the voltage-controlled oscillator operates as a frequency modulator.

15. A tricolor LED display system, comprising:

- 10 a tricolor LED dot matrix display having a plurality of tricolor LEDs;
means for applying a display signal to the tricolor LEDs to generate a visible signal pattern; and
means for applying an audio signal to the tricolor LEDs to emit a modulated visible light signal that carries the audio signal.

15 16. An interface circuit for controlling a tricolor LED display, comprising:

- a row counter decode system coupled to a modulated audio signal for controlling the row selection of the LEDs in the tricolor LED display; and
a segment counter decode system coupled to display information for controlling the segment selection of the LEDs in the tricolor LED display.

20

17. The interface circuit of claim 16, further comprising:

- a parallel port interface for receiving the display information, the display information including display data and addressing information for the LED display;
an address comparison unit for comparing the addressing information for the
25 LED display with system addressing information generated by the row counter decode system and the segment counter decode system; and
a pattern storage system for storing the display data when the address comparison unit indicates that the addressing information for the LED display is the same as the addressing information generated by the row counter decode system and
30 the segment counter decode system.

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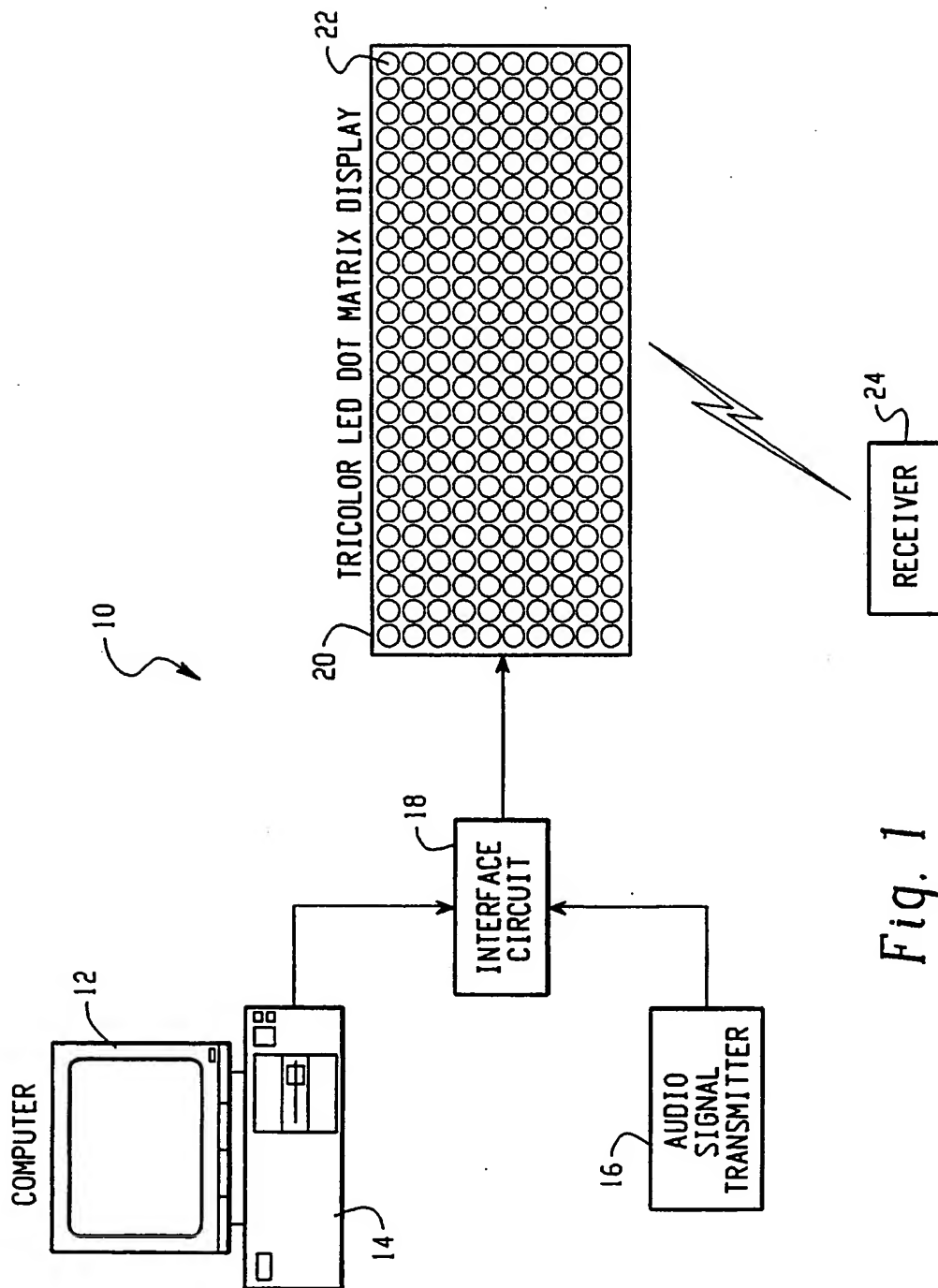
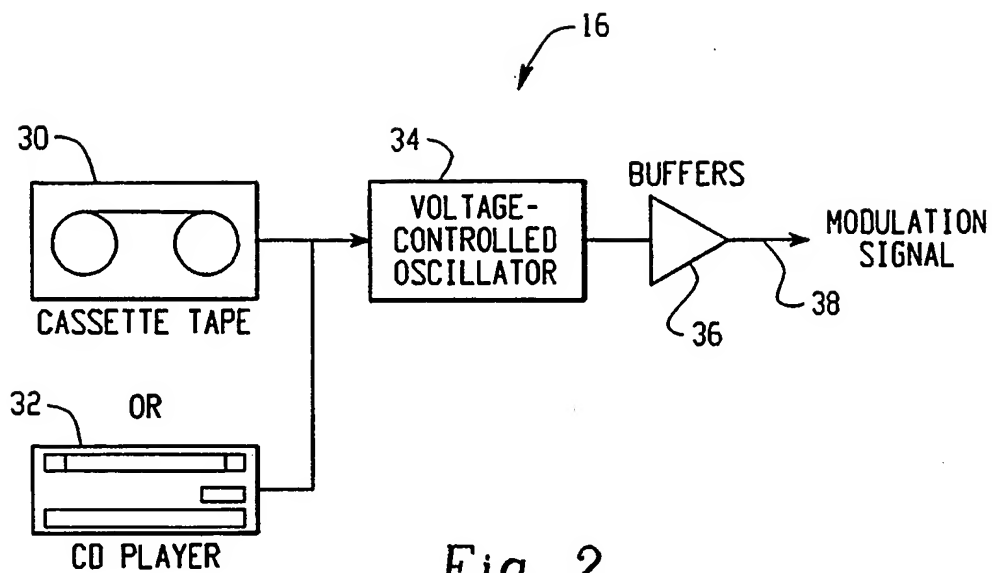
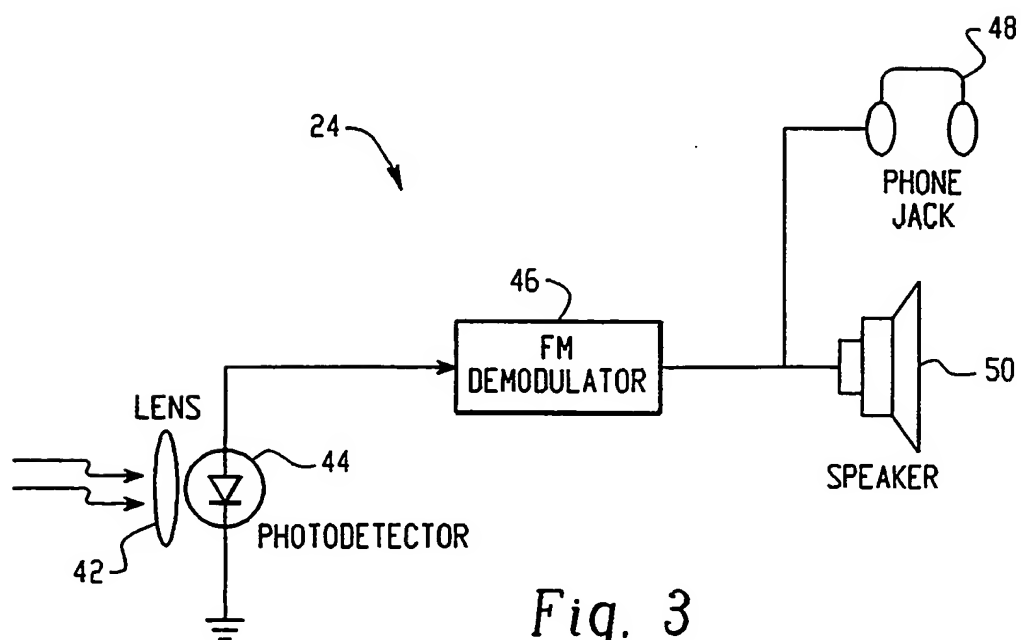
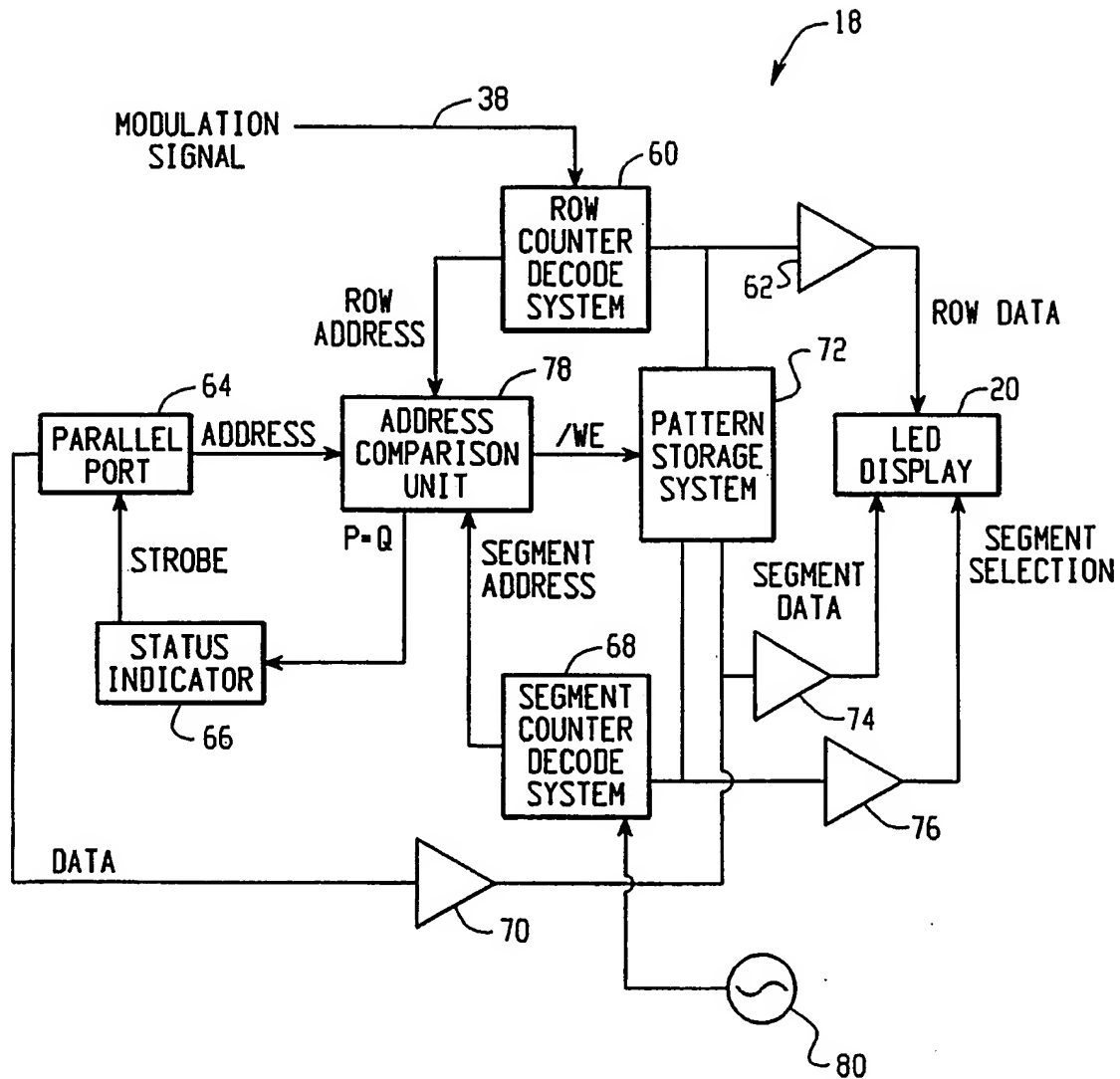


Fig. 1

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*Fig. 2**Fig. 3*

*Fig. 4*

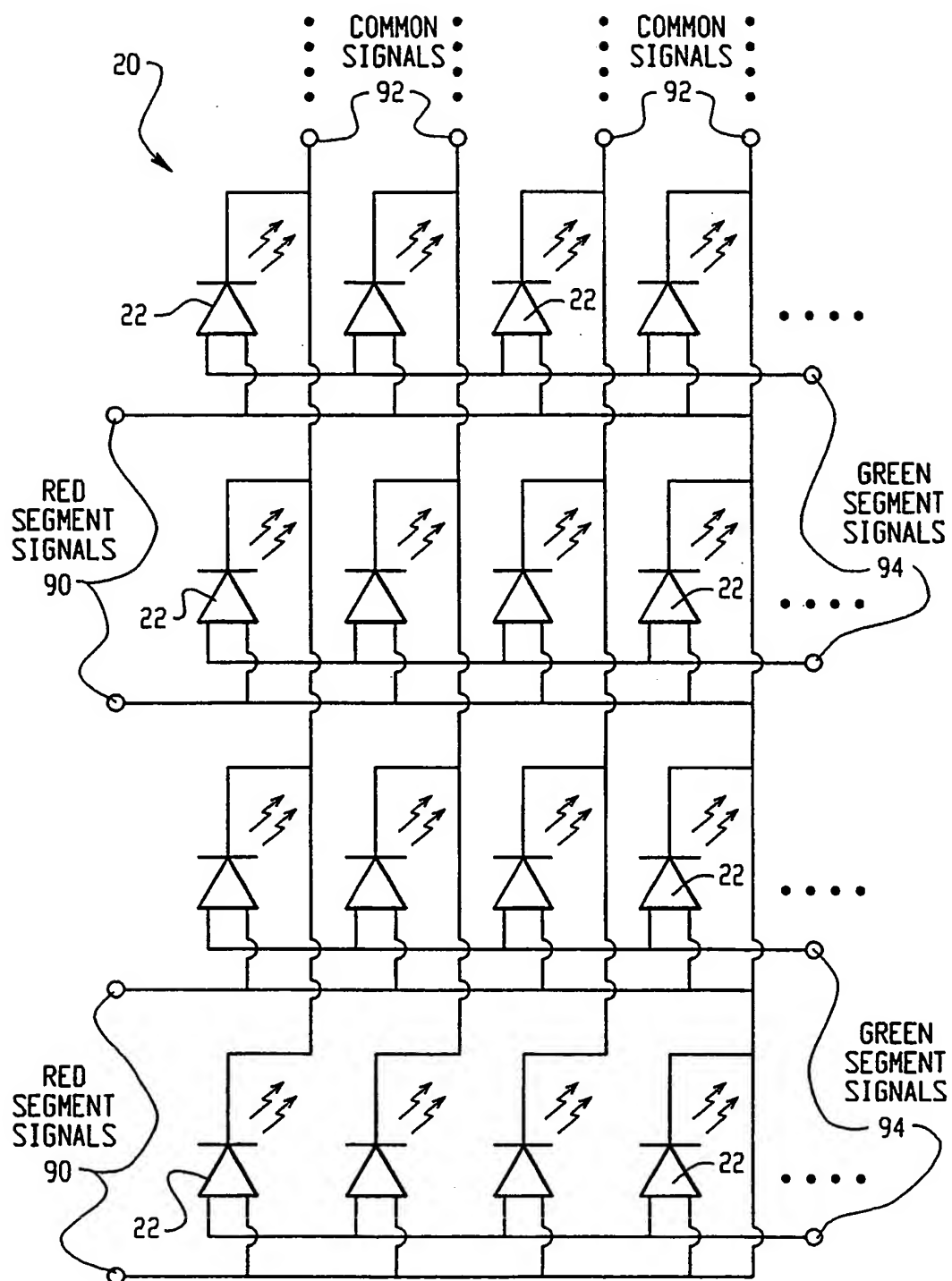


Fig. 5

INTERNATIONAL SEARCH REPORT

International application No.

PCT/IB99/00891

A. CLASSIFICATION OF SUBJECT MATTER

IPC(6) : G09G 3/32

US CL : 345/39, 82, 83, 84; 348/801, 802

According to International Patent Classification (IPC) or to both national classification and IPC

B. FIELDS SEARCHED

Minimum documentation searched (classification system followed by classification symbols)

U.S. : 345/39, 82, 83, 84; 348/801, 802

Documentation searched other than minimum documentation to the extent that such documents are included in the fields searched

Electronic data base consulted during the international search (name of data base and, where practicable, search terms used)

C. DOCUMENTS CONSIDERED TO BE RELEVANT

Category*	Citation of document, with indication, where appropriate, of the relevant passages	Relevant to claim No.
Y	US 5,570,140 A (TOIDE et al) 29 October 1996, col. 1, lines 45-55, col. 21, lines 6-32.	1, 3, 8-10
Y	US 4,841,200 A (CLEARY, Jr. et al) 20 JUNE 1989, figure 1, col. 1, line 65 to col. 2, line 5.	15, 16
Y	US 5,184,114 A (BROWN) 02 February 1993, figures 8, 9.	1, 3, 8-10, 15, 16

☐ Further documents are listed in the continuation of Box C. ☐ See patent family annex.

* Special categories of cited documents:	*T* later document published after the international filing date or priority date and not in conflict with the application but cited to understand the principle or theory underlying the invention
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Authorized officer

REGINA LIANG

Telephone No. (703) 305-4719